

a third external terminal for receiving a write enable signal;

a fourth external terminal for receiving a designating signal [which is used for] designating one of said operational modes; [and]

first means, coupled to said first, second and third external terminals, for detecting levels of said row address strobe signal, said column address strobe signal and said write enable signal; and

second means, coupled to said first means and to said fourth external terminal, for setting said one of said operational modes in response to said designating signal and in response to said column address strobe signal being at a logic "low" level, said write enable signal being at a logic "low" level and said row address strobe signal being at a logic "low" level.

[control means for controlling the operation of access to a memory cell on the basis of said operational mode which is designated,

wherein said operational mode is designated in response to said column address strobe signal being at a logic "low" level, said write enable signal being at a logic "low" level and said signal being at a first predetermined logic level when said row address strobe signal is at a transitional logic level corresponding to a falling edge.]

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15. (Amended) An address multiplex dynamic RAM according to claim 14, wherein said fourth external terminal is used for

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receiving both said signal for designating one of said operational modes and for receiving an address [terminal] signal.

Claim 16, line 2, ~~delete "to a"~~ insert -- into one of said --, ~~delete "cell"~~ insert -- cells --.

Claim 17, line 2, after "said" insert -- one of said --, ~~delete "cell"~~ insert -- cells --.

REMARKS

Reconsideration and allowance of this application, as amended, is respectfully requested.

This Amendment is in response to the Office Action dated October 4, 1994. By the present Amendment, claims 14-17 have been revised to response to the 35 USC 112, second paragraph, rejection set forth in the Office Action, and to further clarify the claim language.

Reconsideration and removal of the 35 USC 112, second paragraph, rejection is respectfully requested. In the Office Action, certain objections were raised with regard to claims 14 and 15. In particular, the rejection indicated that "claim 14 appears to be nothing more than a functional recitation of signal states with no structure recited to perform or respond to the required states." In response to this, claim 14 has now been amended to replace the last two paragraphs with specific first and second means which are coupled to the respective terminals to carry out the operations of detecting certain levels and setting an operational mode in response to